

PARALLEL GENERATOR OF q -VALUED PSEUDORANDOM SEQUENCES BASED ON ARITHMETIC POLYNOMIALS

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ABSTRACT. A new method for parallel generation of q -valued pseudorandom sequence based on the presentation of systems generating logical formulae by means of arithmetic polynomials is proposed. Fragment consisting of k -elements of q -valued pseudorandom sequence may be obtained by means of single computing of a single recursion numerical formula. It is mentioned that the method of the “arithmetization” of generation may be used and further developed in order to protect the encryption gears from cryptographic onset, resulting in the initiating of mass hardware failures. The achieved results may be widely applied to the realization of perspective high-performance cryptographic facilities for information protection.

CONTENTS

1. Introduction	2
2. General information	2
3. Method of parallel generation of q -valued PRS	3
4. Numerical example	6
5. Conclusion	7
References	8

1. INTRODUCTION

Many specialists connect the further development of information protection facilities with the application of multiple-valued function of logical algebra (MVFLA), in particular, with the use of pseudorandom sequences (PRS) over $\text{GF}(q)$ ($q > 2$), which possess a wider scope of unique features, if compared with binary PRS. The most effective and approved way of obtaining PRS is the use of special switching networks called linear feedback recurrent shift register (LFSR) [1–3].

2. GENERAL INFORMATION

Construction of LFSR over $\text{GF}(q)$ (hereinafter q -LFSR) is carried out by means of given characteristic polynomial:

$$P(z) = z^r \oplus p_{r-1}z^{r-1} \oplus p_{r-2}z^{r-2} \oplus \dots \oplus p_0 \pmod{q},$$

where $P(z) \in \text{GF}(q)$, and r is $P(z)$ polynomial order, $r \in N$, and to the constructed according to it recurrent equation:

$$(1) \quad s_{n+r} = p_{r-1}s_{n+r-1} \oplus p_{r-2}s_{n+r-2} \oplus \dots \oplus p_0s_n \pmod{q},$$

$n = 0, 1, 2, \dots$; where $p_j \in \text{GF}(q)$, $0 \leq j \leq r-1$; \oplus – is the symbol of addition according to \pmod{q} .

In general case q -LFSR consists of D_j ($j = 0, 1, \dots, r-1$) cells and has the following initial fill: s_0, s_1, \dots, s_{r-1} . Here the “cell” is the $\lceil \log_2 q \rceil$ parallel stage register ($\lceil x \rceil$ being the least integral number equal or more than x). After the first cycle q -LFSR has the following fill: s_1, s_2, \dots, s_r . In general q -LFSR generates infinite q -valued PRS: $s_0, s_1, s_2, \dots, s_{r-1}, \dots$ [2].

In notation of linear algebra the next q -valued element of PRS s_{n+r} is represented as a product:

$$\begin{pmatrix} s_{n+r} \\ s_{n+r-1} \\ \dots \\ s_{n+2} \\ s_{n+1} \end{pmatrix}^T = \begin{pmatrix} s_{n+r-1} \\ s_{n+r-2} \\ \dots \\ s_{n+1} \\ s_n \end{pmatrix}^T \cdot \begin{pmatrix} p_{r-1} & 1 & 0 & \dots & 0 \\ p_{r-2} & 0 & 1 & \dots & 0 \\ \dots & \dots & \dots & \dots & \dots \\ p_1 & 0 & 0 & \dots & 1 \\ p_0 & 0 & 0 & \dots & 0 \end{pmatrix}.$$

In the Fig. 1 Structural diagram of the sequential q -LFSR functioning is shown.

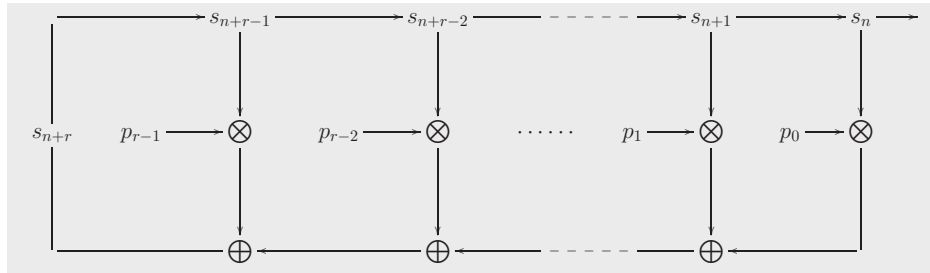


FIGURE 1. Structural diagram of the operation of the sequential q -LFSR in accordance with formula (1) (\oplus and \otimes — according to transaction of addition and multiplication of the \pmod{q})

- number of symbols at the period of PRS or PRS period is defined as $L = q^r - 1$;
- number of similar nonzero symbols in the PRS period is equal to q^{r-1} , and the number of zero symbols is equal to $q^{r-1} - 1$;
- addition of elements in a PRS with elements of the same PRS shifted numbering (except number equal period repetition) gives a similar PRS shifted numbering;
- autocorrelation function of PRS is defined by means of the ratio $p(0) = 1$, $p(i) = -\frac{1}{q^r-1}$, $1 \leq i \leq q^r - 2$, etc.

In the most of practically important cases, besides the “useful” structural quantities, every complex system should be aimed at the achievement of some limiting characteristic or some quality indicator, what can be obtained by means of the minimization of the quantity of operations, using of resources or parallelization of computational processes of the system [4]. So, the paper [5] shows the algorithm of parallelization of generation of binary PRS based on the presentation of systems generating recurrent logical formulae by means of arithmetic polynomials. At the same time the development of computing machinery and software requires the invention of the new approaches to firmware realization both of binary functions and ($q > 2$)-valued functions of logical algebra [6, 7].

$$(2) \quad \begin{cases} s_{n+r} = p_{r-1}s_{n+r-1} \oplus p_{r-2}s_{n+r-2} \oplus \dots \oplus p_0s_n & (\text{mod } q), \\ s_{n+r+1} = p_{r-1}s_{n+r} \oplus p_{r-2}s_{n+r-1} \oplus \dots \oplus p_0s_{n+1} & (\text{mod } q), \\ \vdots \\ s_{n+2r-1} = p_{r-1}s_{n+2r-2} \oplus p_{r-2}s_{n+2r-3} \oplus \dots \oplus p_0s_{n+r-1} & (\text{mod } q), \end{cases}$$

By the analogy with [5], let us express the right parts of the system (2) through the given initial condition:

[illegible]

where a_i is the i -ratio of arithmetical polynomial; $S = s_n, s_{n+1}, \dots, s_{n+r-1}$ are the arguments of MVFLA $s_u \in 0, 1, \dots, q-1$ ($u = 0, 1, \dots, r-1$); $(i_0 \ i_1 \ \dots \ i_{r-1})_q$

$$(i_0 \ i_1 \ \dots \ i_{r-1})_q = \sum_{u=0}^{r-1} i_u q^{r-u-1} \ (i_u \in 0, 1, \dots, q-1);$$

$$s_u^{i_u} = \begin{cases} 1, & i_u = 0, \\ s_u, & i_u \neq 0. \end{cases}$$

To do that, let us coordinate MVFLA (4) system with the system of arithmetical polynomials (5). Then we get:

$$(6) \quad \begin{cases} A_1(S) = \sum_{i=0}^{q^r-1} a_{1,i} s_n^{i_0} s_{n+1}^{i_1} \dots s_{n+r-1}^{i_{r-1}}, \\ A_2(S) = \sum_{i=0}^{q^r-1} a_{2,i} s_n^{i_0} s_{n+1}^{i_1} \dots s_{n+r-1}^{i_{r-1}}, \\ \vdots \\ A_r(S) = \sum_{i=0}^{q^r-1} a_{r,i} s_n^{i_0} s_{n+1}^{i_1} \dots s_{n+r-1}^{i_{r-1}}. \end{cases}$$

[illegible]

Then we get:

$$D(S) = \sum_{i=0}^{q^{r-1}-1} \sum_{l=1}^d a_{l,i}^* s_n^{i_0} s_{n+1}^{i_1} \cdots s_{n+r-1}^{i_{r-1}}.$$

$$(7) \quad M(S) = \bigoplus_{i=0}^{q^{r-1}-1} c_i s_n^{i_0} s_{n+1}^{i_1} \dots s_{n+r-1}^{i_{r-1}} \pmod{q^r},$$
$$c_i = \bigoplus_{l=1}^r a_{l,i}^* \quad (i = 0, 1, \dots, q^{r-1} - 1).$$
$$\Xi^t\{M(S)\} = \left\lfloor \frac{M(S)}{q^t} \right\rfloor \pmod{q},$$

where t is the required q -stage of the representation $M(S)$. Structural diagram of the parallel operation of the generator in accordance with formula (7) has the form (see Fig. 3).

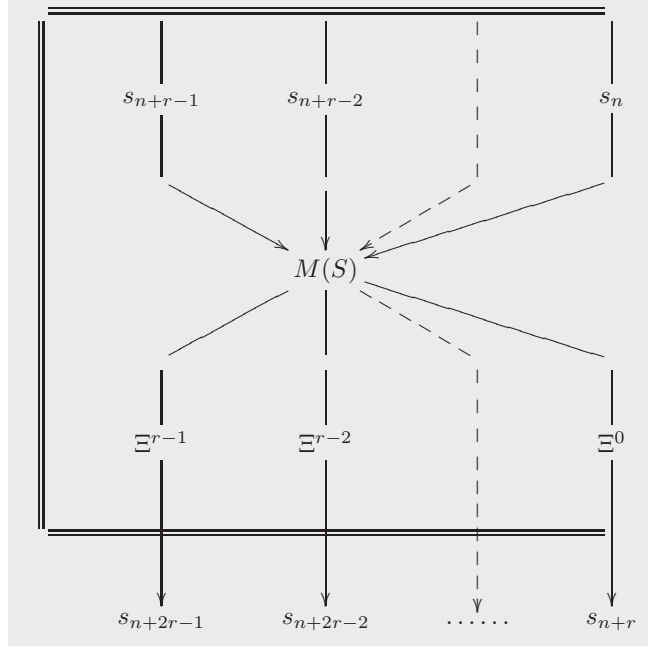


FIGURE 3. Structural diagram of the operation of the parallel q -LFSR in accordance with the arithmetic polynomials (7)

4. NUMERICAL EXAMPLE

Let examine the construction $q = 3$ LFSR, generating 3-digit PRS given by characteristic equation: $s_{k+3} = 2s_{k+2} \oplus s_k \pmod{3}$ and initial fill: $s_0 = 0$, $s_1 = 1$, $s_2 = 2$. The corresponding characteristic polynomial is represented as: $P(z) = z^3 + 2z^2 + 1$.

In this case the system of characteristic equations for the PRS section of three elements will be represented as follows:

$$\begin{cases} s_3 = 2s_2 \oplus s_0 \pmod{3}, \\ s_4 = 2s_3 \oplus s_1 \pmod{3}, \\ s_5 = 2s_4 \oplus s_2 \pmod{3}. \end{cases}$$

Then let us represent the system of characteristic equations as the MVFLA system with right part of equalities, expressed by means of initial given conditions:

$$\begin{cases} f_3(s_2, s_1, s_0) = 2s_2 \oplus s_0 \pmod{3}, \\ f_4(s_2, s_1, s_0) = s_2 \oplus s_1 \oplus 2s_0 \pmod{3}, \\ f_5(s_2, s_1, s_0) = s_0 \oplus 2s_1 \pmod{3}. \end{cases}$$

According to (6) we shall get the system of arithmetical polynomials as follows:

$$\begin{cases} A_3(S) = \frac{1}{4}(14s_2 - 6s_2^2 + 4s_0 - 39s_2s_0 + 21s_0s_2^2 + 15s_0^2s_2 - 9s_0^2s_2^2), \\ A_4(S) = \frac{1}{8}(8s_2 + 8s_1 + 42s_1s_2 - 30s_1s_2^2 - 30s_2s_1^2 + 18s_1^2s_2^2 + 28s_0 - 78s_0s_2 + \\ + 30s_0s_2^2 - 78s_0s_1 + 78s_0s_1s_2 + 30s_0s_1^2 - 18s_0s_1^2s_2^2 - 12s_0^2 + 42s_0^2s_2 - \\ - 18s_0^2s_2^2 + 42s_0^2s_1 - 72s_0^2s_1s_2 + 18s_0^2s_1s_2^2 - 18s_0^2s_1^2 + 18s_0^2s_2s_1^2), \\ A_5(S) = \frac{1}{4}(14s_1 - 6s_1^2 + 4s_0 - 39s_1s_0 + 21s_0s_1^2 + 15s_0^2s_1 - 9s_0^2s_1^2). \end{cases}$$

Let realize the system of arithmetical expressions as arithmetical polynomial:

$$\begin{cases} D(S) = \frac{1}{4}(14s_2 - 6s_2^2 + 4s_0 - 39s_2s_0 + 21s_0s_2^2 + 15s_0^2s_2 - 9s_0^2s_2^2) + \\ + 3^1(\frac{1}{8}(8s_2 + 8s_1 + 42s_1s_2 - 30s_1s_2^2 - 30s_2s_1^2 + 18s_1^2s_2^2 + 28s_0 - 78s_0s_2 + \\ + 30s_0s_2^2 - 78s_0s_1 + 78s_0s_1s_2 + 30s_0s_1^2 - 18s_0s_1^2s_2^2 - 12s_0^2 + 42s_0^2s_2 - 18s_0^2s_2^2 + \\ + 42s_0^2s_1 - 72s_0^2s_1s_2 + 18s_0^2s_1s_2^2 - 18s_0^2s_1^2 + 18s_0^2s_2s_1^2)) + 3^2(\frac{1}{4}(14s_1 - 6s_1^2 + \\ + 4s_0 - 39s_1s_0 + 21s_0s_1^2 + 15s_0^2s_1 - 9s_0^2s_1^2)). \end{cases}$$

Modular polynomial form will be expressed as:

$$\begin{aligned} M(S) = & 7s_0 \oplus 9s_0^2 \oplus 21s_1 \oplus 18s_0s_1 \oplus 9s_0^2s_1 \oplus 18s_0s_1^2 \oplus 20s_2 \oplus 15s_0s_2 \oplus 6s_0^2s_2 \oplus \\ & \oplus 9s_1s_2 \oplus 9s_0s_1s_2 \oplus 9s_1^2s_2 \oplus 12s_2^2 \oplus 3s_0s_2^2 \oplus 18s_0^2s_2^2 \oplus 9s_1s_2^2 \pmod{27}. \end{aligned}$$

According to the given initial conditions we may obtain the following three-digit fragment of PRS:

$$\begin{array}{ll} \text{step 1} \begin{cases} s_3 = \Xi^0\{19\} = 1, \\ s_4 = \Xi^1\{19\} = 0, \\ s_5 = \Xi^2\{19\} = 2; \end{cases} & \text{step 5} \begin{cases} s_{15} = \Xi^0\{5\} = 2, \\ s_{16} = \Xi^1\{5\} = 1, \\ s_{17} = \Xi^2\{5\} = 0; \end{cases} \\ \text{step 2} \begin{cases} s_3 = \Xi^0\{14\} = 2, \\ s_4 = \Xi^1\{14\} = 1, \\ s_5 = \Xi^2\{14\} = 1; \end{cases} & \text{step 6} \begin{cases} s_3 = \Xi^0\{17\} = 2, \\ s_4 = \Xi^1\{17\} = 2, \\ s_5 = \Xi^2\{17\} = 1; \end{cases} \\ \text{step 3} \begin{cases} s_3 = \Xi^0\{10\} = 1, \\ s_4 = \Xi^1\{10\} = 0, \\ s_5 = \Xi^2\{10\} = 1; \end{cases} & \text{step 7} \begin{cases} s_3 = \Xi^0\{4\} = 1, \\ s_4 = \Xi^1\{4\} = 1, \\ s_5 = \Xi^2\{4\} = 0; \end{cases} \\ \text{step 4} \begin{cases} s_3 = \Xi^0\{9\} = 0, \\ s_4 = \Xi^1\{9\} = 0, \\ s_5 = \Xi^2\{9\} = 1; \end{cases} & \text{step 8} \begin{cases} s_3 = \Xi^0\{19\} = 1, \\ s_4 = \Xi^1\{19\} = 0, \\ s_5 = \Xi^2\{19\} = 0; \end{cases} \\ & \dots\dots\dots \end{array}$$

5. CONCLUSION

Here is the representation of one of the possible non-standard methods of realization of parallel algorithm of generation of q -valued PRS, based on the arithmetical representation of MVFLA. The developed algorithm may be used for the realization of perspective high-performance cryptographic facilities for information protection. The further direction of the research is the realization of the developed algorithm

of generation of q -valued PRS using the redundant code redundant number system, which provide control over the errors while computing the PRS elements.

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